



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Young-Mi Tak

Examiner: Hoai V. Pham

Serial No.: 10/718,309

Group Art Unit: 2814

Filed: September 3, 2004

Docket: 8071-49 (OPP021241US)

For: **THIN FILM TRANSISTOR ARRAY PANEL INCLUDING
SYMMETRICALLY ALIGNED THIN FILM TRANSISTORS AND
MANUFACTURING METHOD THEREOF**

MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. 1.312

Examiner:

Please consider the following amendment and remarks before issuance of the above-identified application.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313-1450 on 9/27/2006.

Dated: September 27, 2006


Michael F. Morano

IN THE TITLE

Please make the following amendment:

THIN FILM TRANSISTOR ARRAY PANEL INCLUDING SYMMETRICALLY
~~SYMMETRICALLY~~ ALIGNED THIN FILM TRANSISTORS AND MANUFACTURING
METHOD THEREOF